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FIG. 1
Prior Art

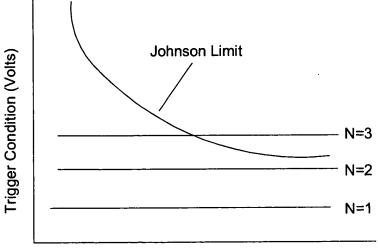
VDD

Clamp
Device

Rbias

Rballast

FIG. 2 Prior Art



Cutoff Frequency f_T (GHz)

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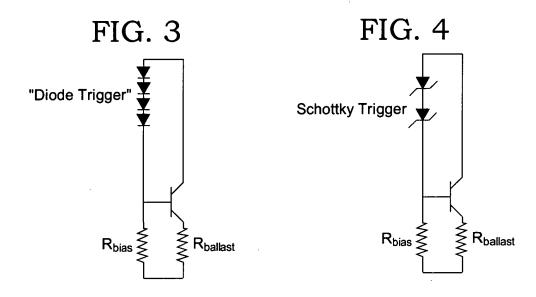
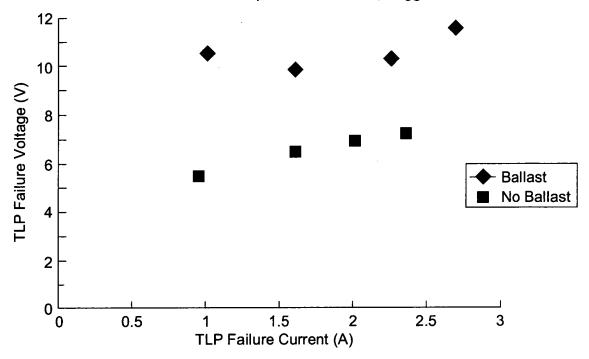


FIG. 5

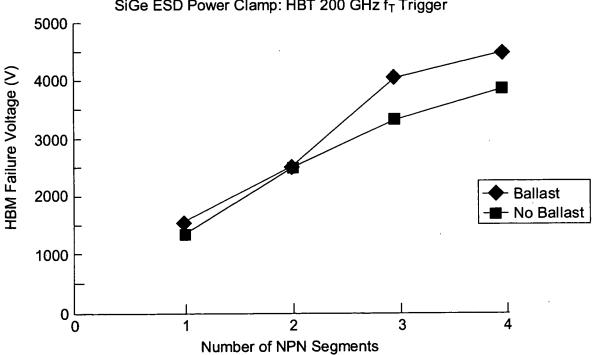
N+ Subcollector

N+ Pedestal

 $\it 3/4$ $\it FIG. 6$ SiGe ESD Power Clamp: HBT 200 GHz f_T Trigger



 $FIG.~7 \\ \text{SiGe ESD Power Clamp: HBT 200 GHz } f_{\text{T}} \text{ Trigger}$



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